

Systèmes embarqués aéronautique et automobile

MASTER

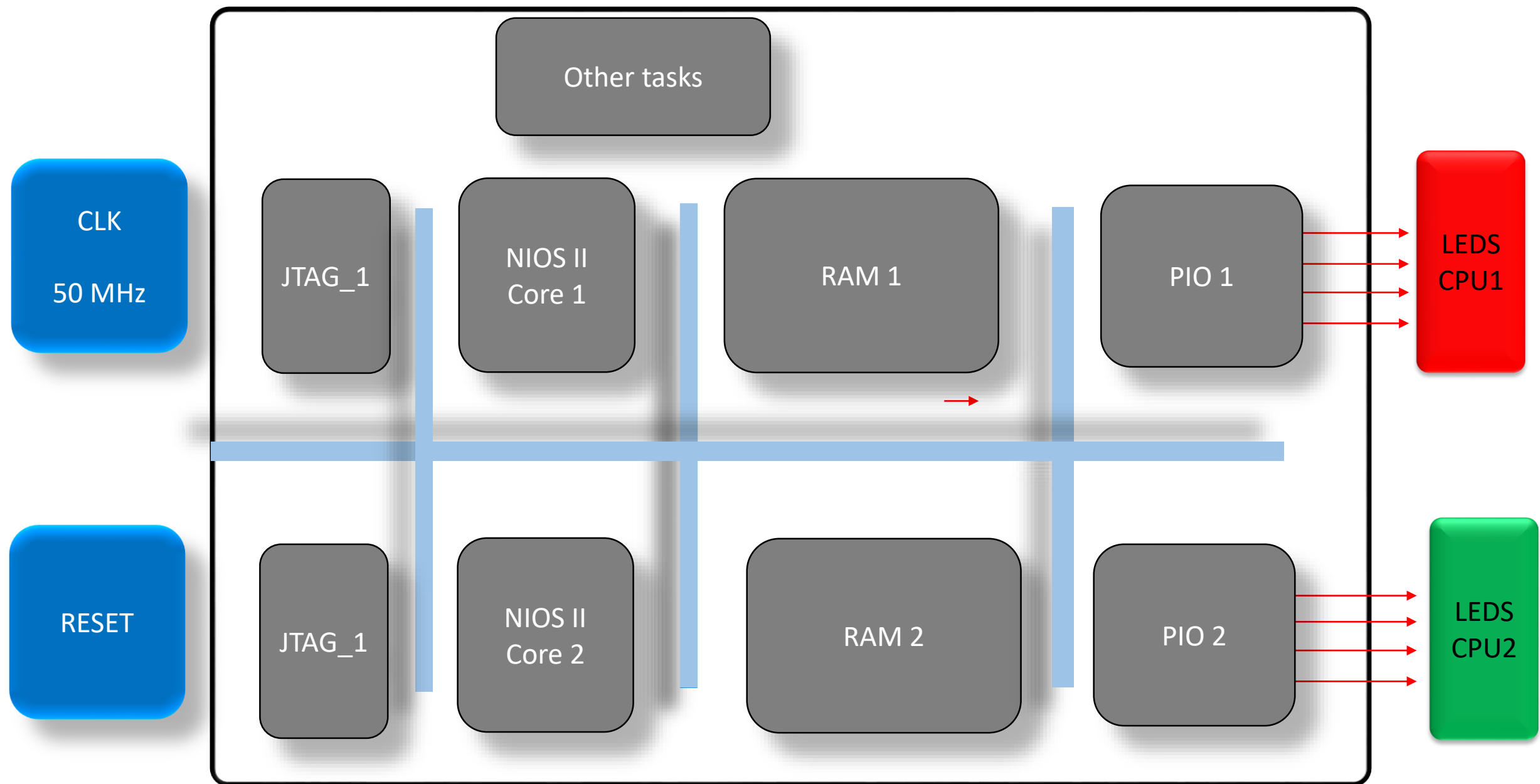
Ingénierie des Systèmes Embarqués

Chapitre 7:

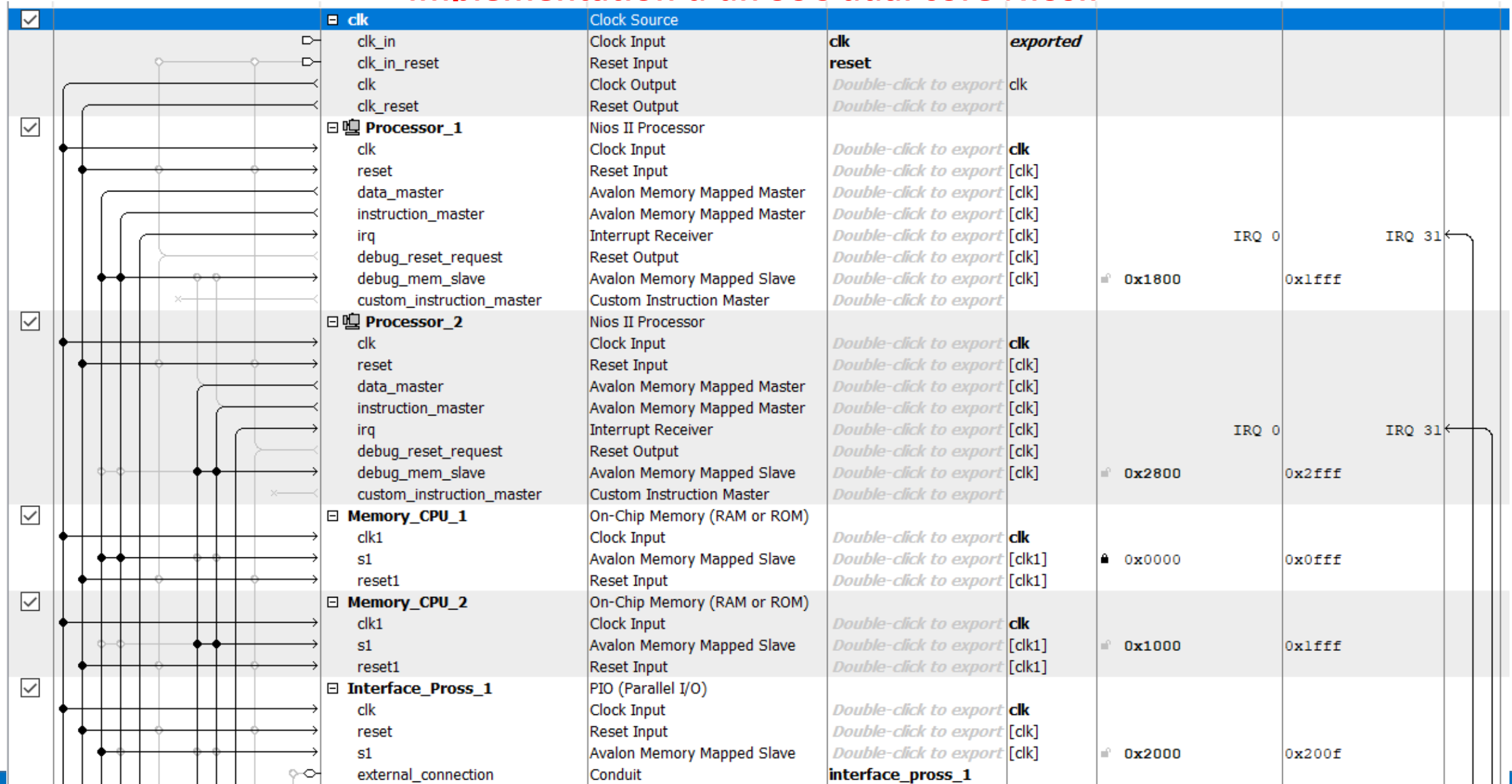
SoC Design: 2 Dual Core Nios II for Parallel Processing

Année universitaire 2025/2026

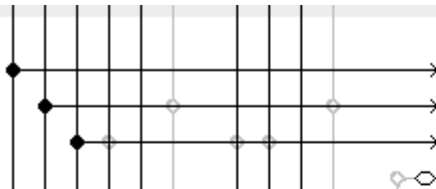
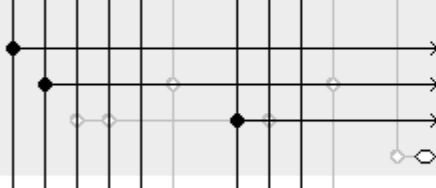
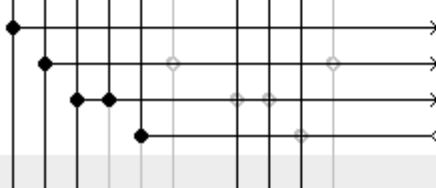
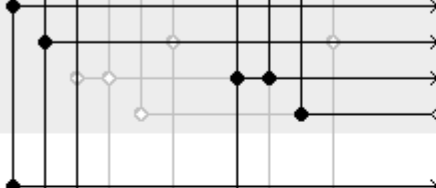
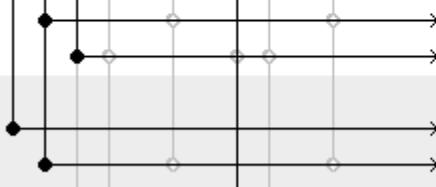
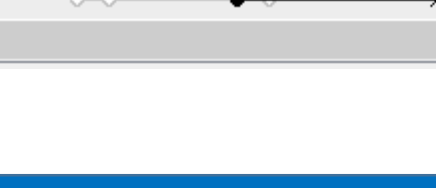
Dr. Ing. LASSIOUI Abdellah



Implémentation d'un SoC dual core NiosII



Implémentation d'un SoC dual core NiosII

✓		Interface_Pross_1 clk reset s1 external_connection	PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i> interface_pross_1	clk [clk] [clk]	0x2000 0x200f	
✓		Interface_Pross_2 clk reset s1 external_connection	PIO (Parallel I/O) Clock Input Reset Input Avalon Memory Mapped Slave Conduit	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i> interface_pross_2	clk [clk] [clk]	0x3000 0x300f	
✓		jtaguart_0 clk reset avalon_jtag_slave irq	JTAG UART Clock Input Reset Input Avalon Memory Mapped Slave Interrupt Sender	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i>	clk [clk] [clk] [clk]	0x2020 0x2027	
✓		jtaguart_1 clk reset avalon_jtag_slave irq	JTAG UART Clock Input Reset Input Avalon Memory Mapped Slave Interrupt Sender	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i>	clk [clk] [clk] [clk]	0x3020 0x3027	
✓		sysid_cpu1 clk reset control_slave	System ID Peripheral Clock Input Reset Input Avalon Memory Mapped Slave	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i>	clk [clk] [clk]	0x2018 0x201f	
✓		sysid_cpu2 clk reset control_slave	System ID Peripheral Clock Input Reset Input Avalon Memory Mapped Slave	<i>Double-click to export</i> <i>Double-click to export</i> <i>Double-click to export</i>	clk [clk] [clk]	0x3018 0x301f	

Implémentation d'un SoC dual core NiosII

